Errata

Publication I

The TFE consumes 69 pJ per pulse at 7.5 GHz, not 63 pJ. The efficiency of the TFE is 2.6 %, not 2.3 %.

The carrier frequency in [11] is tuned through analog driver transistor control, not analog supply voltage control.

Publication II

In Table I, the name of the first author of [2] should be de Streel, not de Steel.

The carrier frequency in [8] is tuned through analog driver transistor control, not analog supply control.

Publication IV

In Fig. 6, the high-frequency oscillations at about 10 ns and 16 ns are echoes of the generated pulse caused by signal reflections at the oscilloscope input and the TFE (pulse generator) output.

Publication V

The TCPLL prototype deploys two Arduino Due boards, not Aruduio Due boards.

In Appendix B, the value of $\max_{T_0 \le T \le T_1} |E_1(T)|$ approaches 0 at around 27 °C, not 10 °C.

Fig. 6 uses $E_n = \kappa - \epsilon$ whereas Appendix B uses $E_n = \epsilon - \kappa$ as per [127, p. 329]. Fig. 6 should also use $E_n = \epsilon - \kappa$ for consistency.

Eq. (11) should be $\max_{T_1 \le T \le T_2} |E_2| = \left| -\frac{2\sqrt{3}}{9} \Delta T^3 \alpha \right|$, not $E_2 = \frac{2\sqrt{3}}{9} \Delta T^3 \alpha$.